

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S138	15	"718"/\$.ccls. and ((suspend\$5 cancel\$5 drop\$6 stop\$6 halt\$5) near4 (execut\$5) same (mutitask\$5 multi-task\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 13:18
L1	5	"709"/\$.ccls. and ((suspend\$5 cancel\$5 drop\$6 stop\$6 halt\$5) near4 (execut\$5) same (mutitask\$5 multi-task\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 13:18
S137	1	S136 and ((suspend\$5 cancel\$5 drop\$6 stop\$6 halt\$5) near4 (execut\$5) same (mutitask\$5 multi-task\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 11:13
S136	531	718/103.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 11:10
S135	10	("5333266" "5479411" "5557659" "5568540" "5604737" "5608786" "5726984" "5742596" "5764901" "6091721").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/06/15 06:46
S134	10	("5333266" "5479411" "5557659" "5568540" "5604737" "5608786" "5726984" "5742596" "5764901" "6091721").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/06/14 17:39
S133	2	"6532230".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:33
S132	70	(suspend\$5) near5 (display\$5 acquisit\$5 record\$5) near5 (data) same error	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:33
S131	0	(suspend\$5) near5 (display\$5 acquisit\$5 record\$5) near5 (serial\$5) near4 (data) same error	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:19
S130	0	(suspend\$5) near5 (display\$5 acquisit\$5 record\$5) near5 (serial\$5) near4 (data) same priorit\$5 same error	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:18
S129	2	(suspend\$5) near5 (display\$5 acquisit\$5 record\$5) near5 (data) same priorit\$5 same error	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:16
S128	10	(priorit\$5) same (mutitask\$5) same (suspend\$5) near5 (execut\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:13

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S12 7	0	(error) same (priorit\$5) same (multitask\$5) same (suspend\$5) near5 (execut\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 17:08
S12 6	13	(error) same (priorit\$5) same (multitask\$5) same (execut\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:59
S12 5	16	(data) near3 (Acqui\$8) same (data) near3 (record\$6) same (display\$5 near4 data) and (priorit\$5) near4 (execut\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:56
S12 3	0	(data) near3 (Acqui\$8) same (data) near3 (record\$6) same (display\$5 near4 data) and (multitask\$5) and (priorit\$5) near4 (execut\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:51
S12 4	793	(data) near3 (Acqui\$8) same (data) near3 (record\$6) same (display\$5 near4 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:49
S12 2	0	(data) near3 (Acqui\$8) same (data) near3 (record\$6) same (display\$5 near4 data) same (priorit\$5) and (multitask\$5) and (priorit\$5) near4 (execut\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:48
S12 1	10	(data) near3 (Acqui\$8 collect\$6 receiv\$5 obtain\$5) same (data) near3 (stor\$5 record\$6) same (display\$5 near4 data) same (priorit\$5) and (multitask\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:47
S12 0	3	(data) near3 (Acqui\$8 collect\$6 receiv\$5 obtain\$5) same (data) near3 (stor\$5 record\$6) same (display\$5 near4 data) same (prior\$9) same (suspend\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:40
S11 9	1100	(data) near3 (Acqui\$8 collect\$6 receiv\$5 obtain\$5) same (data) near3 (stor\$5 record\$6) same (display\$5 near4 data) same (prior\$9)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:36
S11 8	27495	(data) near3 (Acqui\$8 collect\$6 receiv\$5 obtain\$5) same (data) near3 (stor\$5 record\$6) same (display\$5 near4 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:35
S11 7	273878	(data) near3 (Acqui\$8 collect\$6 receiv\$5 obtain\$5) same (data) near3 (stor\$5 record\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 16:25

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S11 3	1	(suspend\$5 cancel\$5 stop\$5) same (program task) same (multi-task\$5) same (priorit\$5) same (lower higher) same (error)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 15:07
S11 2	51	(suspend\$5 cancel\$5 stop\$5) same (program task) same (multi-task\$5) same (priorit\$5) same (lower higher)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/14 15:07
S11 1	149203	(acqui\$8 collect\$5 obtain\$5 receiv\$5 determin\$5 measur\$5) near3 (propert\$6 characterictics voltage current temperature data value) same(record\$5 stor\$5 sav\$6 hold\$6) same (display\$5 present\$5 stag\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/14 15:03

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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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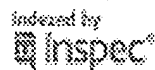
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1 [A scheduling philosophy for multi-processing systems](#)



Butler W. Lampson

 January 1967 **Proceedings of the first ACM symposium on Operating System Principles**

Publisher: ACM Press

Full text available: pdf(1.51 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

One of the essential parts of any computer system is a mechanism for allocating the processors of the system among the various competitors for their services. These allocations must be performed in even the simplest system, for example, by the action of an operator at the console of the machine. In larger systems more automatic techniques are usually adopted; batching of jobs, interrupts and interval timers are the most common ones. As the use of such techniques becomes more frequent, it be ...

2 [A Structural View of PL/I](#)



David Beech

 March 1970 **ACM Computing Surveys (CSUR)**, Volume 2 Issue 1

Publisher: ACM Press

Full text available: pdf(2.86 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Draft Proposed: American National Standard—Graphical Kernel System](#)



Technical Committee X3H3 - Computer Graphics

 February 1984 **ACM SIGGRAPH Computer Graphics**, Volume 18 Issue SI

Publisher: ACM Press

Full text available: pdf(16.07 MB)

Additional Information: [full citation](#)

4 [A scheduling philosophy for multiprocessing systems](#)



Butler W. Lampson

 May 1968 **Communications of the ACM**, Volume 11 Issue 5

Publisher: ACM Press

Full text available: pdf(2.11 MB)

Additional Information: [full citation](#), [references](#), [citations](#)

Keywords: interlocks, interrupt systems, multiprocessing, priority, process, protection, scheduling, time-sharing

5 Fault Tolerant Operating Systems



Peter J. Denning

December 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 4

Publisher: ACM Press

Full text available:  pdf(2.69 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)


6 Illustrative risks to the public in the use of computer systems and related technology



Peter G. Neumann

January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Publisher: ACM Press

Full text available:  pdf(2.54 MB) Additional Information: [full citation](#)


7 A system for computer music performance



David P. Anderson, Ron Kuivila

February 1990 **ACM Transactions on Computer Systems (TOCS)**, Volume 8 Issue 1

Publisher: ACM Press

Full text available:  pdf(2.21 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

A computer music performance system (CMPS) is a computer system connected to input devices (including musical keyboards or other instruments) and to graphic and audio output devices. A human performer generates input events using the input devices. The CMPS responds to these events by computing and performing sequences of output actions whose intended timing is determined algorithmically. Because of the need for accurate timing of output actions, the scheduling requirements of a CMPS differ ...

8 Graphic time-sharing with real-time data bases



Jesse B. Hillman

August 1969 **Proceedings of the 1969 24th national conference**

Publisher: ACM Press

Full text available:  pdf(770.17 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

This system is being developed to process flight test data for the McDonnell-Douglas Corporation to significantly reduce flight test development and certification time and to reduce data processing costs. It is a time-sharing system using graphic cathode ray tube terminals. The system consists of nine processors: a Sigma 7 central processing unit, three general-purpose input/output processors, two special telemetry decommutator channels and three Sigma 2 central processor units. < ...

9 Introduction to Demos



Graham Birtwistle

December 1981 **Proceedings of the 13th conference on Winter simulation - Volume 2**

Publisher: IEEE Press

Full text available:  pdf(1.11 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Demos [1,2] is yet another discrete event simulation language hosted in Simula. It was released in 1979 and is now running on IBM, DEC, UNIVAC, and CDC hardwares amongst others. The paper contains a short introduction to Simula's object and context features; an explanation of the process approach to simulation; a brief comparison of Simula and GPSS; and finally, the main features of Demos are presented via an example.

10 A personal view of the personal work station: some firsts in the Fifties



Douglas Ross

January 1986 **Proceedings of the ACM Conference on The history of personal workstations**

Publisher: ACM Press

Full text available:  pdf(4.26 MB)

Additional Information: [full citation](#), [references](#), [index terms](#)

11 Predictive engineering models based on the EPIC architecture for a multimodal high-performance human-computer interaction task



David E. Kieras, Scott D. Wood, David E. Meyer

September 1997 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 4 Issue 3

Publisher: ACM Press

Full text available:  pdf(368.70 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Engineering models of human performance permit some aspects of usability of interface designs to be predicted from an analysis of the task, and thus they can replace to some extent expensive user-testing data. We successfully predicted human performance in telephone operator tasks with engineering models constructed in the EPIC (Executive Process-Interactive Control) architecture for human information processing, which is especially suited ...

Keywords: cognitive models, usability engineering


12 The Trellis programming environment



Patrick D. O'Brien, Daniel C. Halbert, Michael F. Kilian

December 1987 **ACM SIGPLAN Notices , Conference proceedings on Object-oriented programming systems, languages and applications OOPSLA '87**, Volume 22 Issue 12

Publisher: ACM Press

Full text available:  pdf(1.14 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The Trellis programming environment supports programming in Trellis/Owl, an object-based language with multiple inheritance and compile-time type-checking. Trellis is composed of a number of integrated tools that share a common programming environment database. It is a highly interactive, easy-to-use programming environment, providing various programming aids, incremental compilation, and good debugging support. Trellis is both integrated and open-ended. Trellis was specifically ...

13 Risks to the public in computers and related systems



Peter G. Neumann

April 1990 **ACM SIGSOFT Software Engineering Notes**, Volume 15 Issue 2

Publisher: ACM Press

Full text available:  pdf(2.07 MB)


Additional Information: [full citation](#), [index terms](#)

14 Simulation language design: The process view of simulation in Ada

Greg Lomow, Brian Unger

December 1982 **Proceedings of the 14th conference on Winter Simulation - Volume 1**

Publisher: Winter Simulation Conference

Full text available:  [pdf\(1.39 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

Previously, the process view of simulation, which represents a model as a set of competing and cooperating entities, had been most successfully implemented in the general purpose language SIMULA. This paper describes such a system which is currently being implemented in ADA. ADA's suitability as the base language for such a package is first discussed followed by a description of the facilities offered in SAMOA. (Simulation and Modelling on Ada). SAMOA is a fully integrated, general purpose, d ...

15 Risks to the public in computers and related systems

 Peter G. Neumann

January 1990 **ACM SIGSOFT Software Engineering Notes**, Volume 15 Issue 1

Publisher: ACM Press


Full text available:  [pdf\(2.11 MB\)](#) Additional Information: [full citation](#)

16 Information technology and dataveillance

 Roger Clarke

May 1988 **Communications of the ACM**, Volume 31 Issue 5

Publisher: ACM Press

Full text available:  [pdf\(1.89 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Data surveillance is now supplanting conventional surveillance techniques. With this trend come new monitoring methods such as personal dataveillance and mass dataveillance that require more effective safeguards and a formal policy framework.

17 Ada for closely coupled multiprocessor targets

 A. Cholerton


January 1989 **Proceedings of the conference on Tri-Ada '89: Ada technology in context: application, development, and deployment**

Publisher: ACM Press

Full text available:  [pdf\(1.27 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


The techniques for cross-compiling real-time Ada programs for embedded targets are well developed. Generally, these toolsets enable the user to compile and build a program on the host, load it into the target's memories via some form of serial or parallel link, and then run and debug the program under intensive control from the host. This technology has now been extended by SD to provide similar facilities for a class of closely coupled multiprocessor targets comprising homogeneous ...

18 Multi-model parallel programming in psysche

 M. L. Scott, T. J. LeBlanc, B. D. Marsh

February 1990 **ACM SIGPLAN Notices , Proceedings of the second ACM SIGPLAN symposium on Principles & practice of parallel programming PPOPP '90**, Volume 25 Issue 3

Publisher: ACM Press

Full text available:  [pdf\(1.48 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Many different parallel programming models, including lightweight processes that

communicate with shared memory and heavyweight processes that communicate with messages, have been used to implement parallel applications. Unfortunately, operating systems and languages designed for parallel programming typically support only one model. Multi-model parallel programming is the simultaneous use of several different models, both across programs and within a single program. This paper describes mu ...

19 The evolution of HPC/VORX



H. P. Katseff, R. D. Gaglianello, B. S. Robinson

February 1990 **ACM SIGPLAN Notices , Proceedings of the second ACM SIGPLAN symposium on Principles & practice of parallel programming PPOPP '90**, Volume 25 Issue 3

Publisher: ACM Press

Full text available:  pdf(1.13 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

HPC/VORX is a computing system that provides closely coupled computing between large numbers of processors. It also supports the connection of many host workstations which may be geographically distributed within the area of a large building and allows a single applications to span many processors and many workstations. We relate some of the lessons that were learned while building and using HPC/VORX and in the transition to HPC/VORX from a smaller, less capable system. The problems that we ...

20 The costs of personal computing in a complex organization: a comparative study



Sonia Nayle, Walt Scacchi

December 1986 **ACM SIGOIS Bulletin , Proceedings of the third ACM-SIGOIS conference on Office information systems**, Volume 7 Issue 2-3

Publisher: ACM Press

Full text available:  pdf(1.30 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The widespread adoption of personal computers (PCs) may be attributable to their apparent low purchase and operational costs. However, significant procedural costs arise in fitting a PC application into a work setting. Our investigation of the adoption and use of PCs in several departments of a complex organization reveals a large number of unanticipated costs. These indirect, deferred, and governance costs are chiefly borne by users not responsible for acquiring PCs. These costs represent ...

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